



© Alexandr Mitiuc - Fotolia.com

SYSTEM LEVEL DESIGN FOR HETEROGENEOUS SYSTEMS

With our services and expert tools, we provide a comprehensive support for our customers in all aspects of heterogeneous system design including concept engineering, simulation, and verification. Our support ensures short development cycles and a high quality of the customer's design processes in order to achieve competitive advantages. Our expert tools have proven their value daily, when used by engineers from diverse industrial branches. They are result of our long standing experience in SystemC, SystemC AMS, VHDL, and the comprehension of our industrial partners' requirements.

Our Services

- Comprehensive modeling support
 - Heterogeneous system modeling
 - Virtual prototyping of analog and mixed signal hard- and software
 - Import of existing model IPs to SystemC (e.g. from VHDL)

- TLM modeling
- IP-protected customer models
- Verification services (UVM-based, UML state charts)
- Hardware-in-the-loop simulation and test
- Customer specific design environments, flows and interfaces
- Consulting and support

Your Benefits

- Easy handling of complex models
- Very fast simulation and verification
- Optimized design flows
- Highly effective system level verification
- Long standing experience with SystemC / SystemC AMS
- Improved customer-supplier relations due to IP-protected model exchange

**Fraunhofer Institute for
Integrated Circuits IIS
Division Engineering of Adaptive
Systems EAS**

Zeunerstrasse 38
01069 Dresden, Germany

Contact:
Stephan Gerth
Phone +49 351 4640-847
stephan.gerth@eas.iis.fraunhofer.de

www.eas.iis.fraunhofer.de/en.html